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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of : December 28, 2001
Scott et al. :
Serial No. : Examiner:
Filed: Herewith : IBM Corporation
Title: METHOD FOR IDENTIFYING TEST
POINTS TO OPTIMIZE THE TESTING OF
INTEGRATED CIRCUITS USING A GENETIC
ALGORITHM : Dept. 18G/Bldg. 300-482
2070 Route 52
Hopewell Junction,
New York, 12533

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents and Trademarks
Washington, D.C. 20231

Sirs:

In compliance with the duty of disclosure under
37 C.F.R. § 1.56 and in accordance with the practice under
37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to
the documents listed on the enclosed Form PTO-1449. Copies of the
listed documents are also enclosed.

It is respectfully requested that the above information be
considered by the Examiner and that a copy of the enclosed Form
PTO-1449 be returned indicating that such information has been
considered.

Applicants' undersigned attorney may be reached by telephone
at (845) 894-2481. All correspondence should continue to be

directed to the below listed address.

Respectfully submitted,

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